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RADEMARK		Apı	plication Number	10/619,738	}		
		Fili	ng Date	July 14, 2003			
TRANSMITTAL FORM		Fire	st Named Inventor	Sabhapathi Naidu Annamaneni			
(to be used for all correspondence during pe filed application)	endency of	Group Art Unit Number		2812			
		Examiner Name		Not yet known			
Total Number of Pages in This Submission	5 *	Att	orney Docket Number	22272-080	30		
ENCLO	OSURES	(C	heck all that apply	<i>(</i>)			
Fee Transmittal Form (in duplicate) Check Enclosed Return Receipt Postcard Response to Notice to File Missing Parl Assignment & Recordation Cover Shee Declaration Power of Attorney Application Data Sheet Information Disclosure Statement & PT Copies of IDS Cited Reference Request for Corrected Filing Receipt Request for Correction of Recorded Assignment/Response: After Final Status Request Revocation and Substitute Power of Atter REMARKS: * Does not include pages in reference.	O/SB/08A ces signment		Issue Fee Transr Letter to Chief Dr	mittal raftsperson s): c) of Figure(s) [cication to Board cication to Grou Brief, Reply Bri Priority Docum	of Appe p ief) nent(s)		
SIGNAT	TURE OF	ATI	ORNEY OR AGEN	NT			
Signature: Michaelle					r		
Attorney/Reg. No.: Michael W. Farn, Reg	j. No. 41,01	5		Dated:	JW.	. 20	, 2005
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Sabhapathi Naidu Amamaneni and Sarangkumar Sharadchandra

Bhavsar

APPLICATION NO.:

10/619.738

FILING DATE:

July 14, 2003

TITLE:

System and Method for Automating Integration of Semiconductor

Work in Process Updates

EXAMINER:

Not yet known

GROUP ART UNIT:

2812

ATTY. DKT. NO.:

22272-08030

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By

Michael W. Farn, Reg. No. 41,015

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT Under 37 CFR §§ 1.56 and 1.97-98

SIR:

Pursuant to the provisions of 37 CFR §§ 1.56 and 1.97-98, enclosed herewith is modified form PTO/SB/08A listing references for consideration by the Examiner.

The filing of this Information Disclosure Statement shall not be construed as a representation regarding the completeness of the list of references, or that inclusion of a reference in this list is an admission that it is prior art or is pertinent to this application, or that a search has been made, or as an admission that the information listed is, or may be considered to be, material to patentability, or that no other material information exists, and shall not be construed as an admission against interest in any manner.

This Information Disclosure Statement is being filed:

\boxtimes	within three months of the filing date of the application, or date of entry into
	the national stage of an international application, or before the mailing date of
	a first office action on the merits, whichever event last occurred;

before the mailing of a first official action after the filing of a request for
continued examination (RCE) under 37 CFR § 1.114;

		of the fifting date of this national application of the date of
-		ational stage in an international application, or after the mailing
		est official action on the merits, whichever event last occurred, but
		tiling date of the first to occur of either: (1) a final action under
	-	13; or (2) an action that otherwise closes prosecution in the
applic	ation, a	
	attac	hed hereto is the fee set forth under 37 CFR §1.17(p) for
	subm	aission of this Information Disclosure Statement under 37 CFR.§
	1.97((c); OR
	Appl	icant certifies pursuant to 37 CFR § 1.97(e) that:
,		each item of information contained in this Information
		Disclosure Statement was first cited in a communication from
		a foreign patent office in a counterpart foreign application not
		more than three months prior to the filing of this Statement;
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		foreign patent office in a counterpart foreign application and,
		to the knowledge of the person signing this certification after
,		making reasonable inquiry, no item of information contained
		in this Statement was known to any individual designated
		under 37 CFR § 1.56(c) more than three months prior to the
		filing of this Statement;
on or	before	the payment of the issue fee but after the mailing date of the first
to oc	cur of e	ither: (1) a final action under 37 CFR § 1.113; (2) a notice of
allow	ance u	nder 37 CFR § 1.311; or (3) an action that otherwise closes
prose	cution	in the application, and:
	Appl	icant certifies pursuant to 37 CFR. § 1.97(e) that:
		each item of information contained in this Information
		Disclosure Statement was cited in a communication from a
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	attached hereto is the fee set forth under 37 CFR §1.17(p) for	
	submission of this Information Disclosure Statement under 37 CFR	.§
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	after the payment of the issue fee. Applicant requests that the information	
	contained in this Information Disclosure Statement be placed in the file	
	according to 37 CFR § 1.97(i), although the information may not be	
	considered by the USPTO.	
	Enclosed is a copy of each listed reference that may be material to the examination	of
	this application, and for which there may be a duty to disclose.	
	This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior	
_	application No, filed on, and the references cited therein are here	hv
	referenced, but are not required to be provided in this application under 37 CFR §	.Uy
	1.98(d).	
\boxtimes	• •	£
	This application was filed after June 30, 2003. Therefore, pursuant to the waiver of the requirements under 27 CER 1.08(a)(2)(i) parties of each LLS. Petert and each	'1
	the requirements under 37 CFR 1.98(a)(2)(i), copies of each U.S. Patent and each	
	U.S. Patent Application Publication are not required to be submitted. Copies of an	y
_	foreign patent documents and non-patent literature cited herein are enclosed.	
	Each item of information contained in this Information Disclosure Statement was	•
	cited in a communication from a foreign patent office in a counterpart application,	
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	1.56(c) more than thirty days prior to the filing of this Information Disclosure	
	Statement. 37 CFR § 1.704(d).	
\boxtimes	Applicant submits that no fee is required for the consideration of this Information	
	Disclosure Statement.	
Cons	sideration of the listed references and favorable action are solicited.	
	Respectfully submitted,	
	SABHAPATHI NAIDU ANNAMANENI et al.	
	JAN. 20, 2005 By: phobaelu,	•
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Substitute for form	1449A/PTO		Complete if Known
INFORMATION	NISCI OSLIDE	Application No.	10/619,738
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Filing Date	July 14, 2003
		First Named Inventor	Sabhapathi Naidu Annamaneni
		Art Unit	2812
JAN 2 , 2005 3		Examiner Name	Not yet known
	of 1	Attorney Docket Number	22272-08030

U.S. PATENT DOCUMENTS						
		Document No.				
Examiner Initials*	Cite No.1	Number – Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		

FOREIGN PATENT DOCUMENTS					
		Foreign Patent Document			
Examiner Initials*	Cite No.1	Country Code ³ – Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	L _e

		OTHER REFERENCES – NON-PATENT LITERATURE DOCUMENTS	
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	1	Hagen, Dr. Klaus ten et al., Technical Overview: WIP Tracking IC Manufacturing workflow and	
		Cycle-time Reduction, eSilicon Corporation, Sunnyvale, USA. Available Aug. 6, 2001 at	
		http://www.esilicon.com, 25 pages.	

Examiner Signature	Date Considered	

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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